

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	37042	(processor ((mult\$4 plural\$4) near3 processor)) with (pin pad line interface port) with (controller adapter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L2	769	(processor ((mult\$4 plural\$4) near3 processor)) with (pin pad line interface port) with (controller adapter) with (assign\$5 allocat\$4 define)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L3	1051	(processor ((mult\$4 plural\$4) near3 processor)) with ((pin pad line interface port) same (assign\$5 allocat\$4 define map)) with (controller adapter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L4	892	(processor ((mult\$4 plural\$4) near3 processor)) with ((pin pad line interface port) same (assign\$5 map\$5)) with (controller adapter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L5	49	((mult\$4 plural\$4) near3 processor) with ((pin pad line interface port) same (assign\$5 map\$5)) with (controller adapter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L6	772	L4 and @ad<="20031030"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L7	25	pin same assign\$5 same tool same processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L8	18	pin near3 assign\$5 same tool same processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L9	93	pin near3 assign\$5 with processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58

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L10	82	pin near2 assign\$5 with processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L11	44	pin adj assign\$5 with processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L12	1	("5018197").PN.	USPAT	OR	OFF	2006/06/23 15:58
L13	5789	pin near3 assign\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L14	1151	pin near3 assign\$5 with (processor network interface device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L15	955	L14 and @ad<="20031030"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L16	128	((input/output I/O input output) near3 pin) near3 assign\$5 with (processor network interface device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L17	97	L16 and @ad<="20031030"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L18	8157	pin with assign\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L19	182	pin with assign\$4 with detect\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58

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L20	280	pin with assign\$4 with (detect\$4 request)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L21	110	pin with assign\$4 with (request)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L22	33859	input near3 output near3 controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L23	35406	(input near3 output near3 controller) IOC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L24	9835	(input adj output adj controller) IOC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L25	227	(pin pad) with L24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L26	5	flexible adj ((input adj output adj controller) IOC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L27	53	L25 with (assign\$4 detect\$4 request flexible)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L28	183	L25 and (assign\$4 detect\$4 request flexible)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L29	1309	((input adj output adj controller) IOC) with (first second multiple plurality)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58

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L30	625	((input adj output adj controller) IOC) near3 (first second multiple plurality)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L31	63	L30 with (detect assign request)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L32	1	L30 with ((detect assign request) near3 pin)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L33	20	L30 with (pin)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L34	20	L30 with ((detect assign request) near3 (pin terminal signal line pad))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L35	203	L30 with (pin terminal signal line pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L36	34	L30 with ((detect assign request) with (pin terminal signal line pad))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58
L37	15	producer with consumer with processor with queue	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 15:58